DZLPHIN DESIGN



Vision Al accelerator IP

Effortlessly deploy sub-mW vision tasks at the very edge

Expend the least energy to get actionable insights quickly without compromising on performance





EVERYTHING YOU NEED FROM EXPLORATION TO DEPLOYMENT

Al vision made possible and simple for battery-powered SoCs



Object classification



Object detection & tracking



Image segmentation

No data scientist, No problem

Get started with our ML Model Library
Tap into our data science ecosystem partners

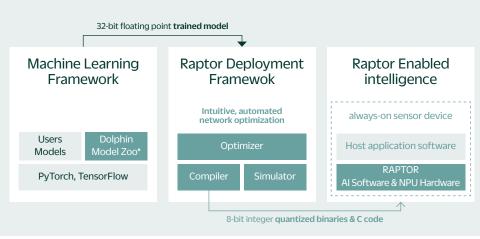
Explore speed & power trade-offs with ease
Export robust deployable Al code in a single click

Scalable Al accelerator IP (NPU)

RTL IP relying on near memory computing architecture
Over 60% MAC utilization at low power

Deploying robust and cost-effective AI at far edge involves a smart combination of hardware and software

Built-in toolbox for taking proper "informed decisions" to build powerful AI model that fits in an area constrained SoC





*available Q1 2023

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