

Missing EDA Links

SLED 2.0

DOLPHIN INTEGRATION

Increased Link with SMASH Simulator

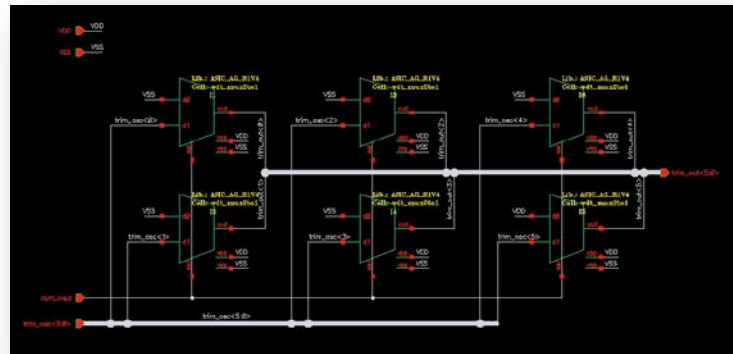
Efficient Schematic Edition

SLED 2.0 extends the link between the schematic editor and the mixed-signal simulator SMASH by providing the means to automatically re-run simulations following parameter updates in the schematic to achieve the optimized design goals.

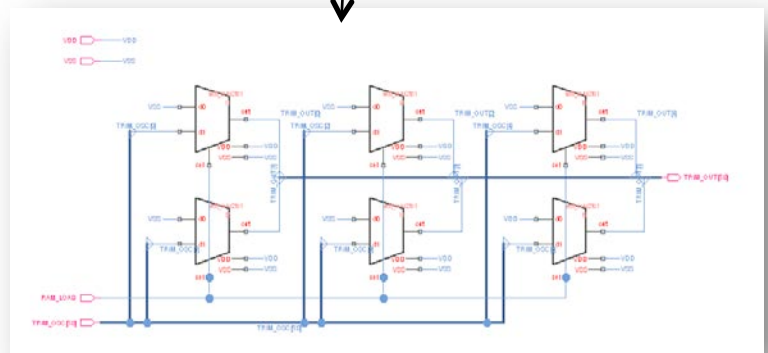
SLED 2.0 also delivers ergonomics and ease of use enhancements to constantly improve the overall user experience and productivity.

Key Enhancements of the Schematic Editor SLED 2.0

- ✓ Automatic update of simulation results following parameter modifications in the schematic
- ✓ Display of waveform results directly in SLED
- ✓ New model library including generic behavioral models of amplifiers, converters, power regulators...
- ✓ Enhanced and documented EDIF import from Cadence
- ✓ User configurable shortcut keys
- ✓ Numerous ease-of-use enhancements
 - Multiple wire creation
 - Optimized schematic drag
 - Smart wire naming
 - Assisted aligning of objects
 - User friendly pin order set up
 - Improved net label modifications (text size, orientation, placement...)
 - High quality schematic copy and paste to external applications



EDIF Import from Cadence



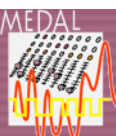
New Behavioral Model Library in SLED

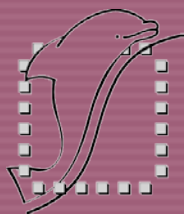
A new library of generic behavioral models has been added in SLED 2.0 enabling efficient design of testbenches and application schematics. This library contains models of amplifiers, converters, power regulators...

Each model is delivered with a symbol, a datasheet accessible directly through the SLED user interface as well as with some testbenches.

Of course, designers have the capability to edit model parameters to their specific requirements.

 **SLED is available identically under Linux and Windows**





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SLED 2.0

Automatic calculation
for designers' productivity

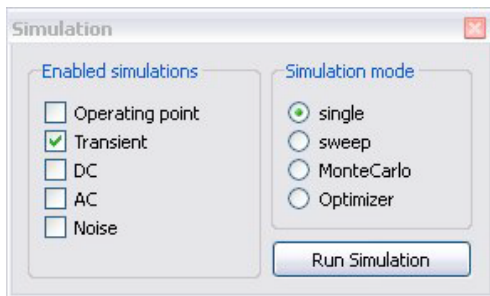
DOLPHIN INTEGRATION

PRELIMINARY

During the “tuning” phase of a design or an application schematic, designers often try several sets of parameters to achieve the best performance. These trial and error steps cost time as designers have to re-run the simulations each time, store the results and compare them at the end.

SLED 2.0 greatly improves designers productivity thanks to an automatic launching of simulations after parameter updates. The results are summarized in a table for an overview of all the trials.

The best combination of parameters can then be selected by the user and the schematic is automatically updated!



1. Configure automatic calculation

2. Modify the instance parameters

3. Check the results

Simulation results

Circuit: filter
Configuration: Smash/Spice

Changes	R20:R	R10:R	R21:R	R11:R	C10:C	R20	C10	opti
Run 0	RVAL	100k	100k	100k	CVAL	→ 20k	5n	-645.79m
Run 1	RVAL	100k	100k	100k	50	→ 20k	50	-1.056m
Run 2	5k	100k	100k	100k	50	→ 5k	50	-921.08u
Run 3	90k	50k	80k	110k	50	→ 90k	50	-1.2353m

Property editor

Instances
Multiple selection

Features
Programmed Connections
Parameters

Name	Value
Common	
Multi	
R	80k;90k;100k;110k
TC1	

Filter.tran.ivs

0s 8ms 16ms 24ms

V(IN) 0 1

V(A1) -8.99829m 500m

V(A0) -986.359u -500m

64ms 72ms 80ms 88ms 96ms

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